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per second, while the design with chip B will have a throughput of $32 \times 50 \text{ w.u./s} = 1,600$ work units per second. So, the total throughput is also greater if we base our design on chip B. This is because the total cost of the chip-B-based design is about the same (near the \$50k target), but the cost-performance of chip B was greater.

SOLUTIONS TO PRACTICE PROBLEMS
C ORGANIZATION AND A

4 Solutions Solution 4.1 4.1.1 The values of the signals are as follows:
RegWrite MemRead ALUMux

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MemWrite ALUOp RegMux Branch a.
1 0 0 (Reg) 0 Add 1 (ALU) 0 b. 1 1 1
(Imm) 0 Add 1 (Mem) 0 ALUMux is the
control signal that controls the Mux
at the ALU input, 0 (Reg) selects the
output of the register file and 1
(Imm) selects the immediate from the

Solution 4 - UCR Computer Science
and Engineering

This means that the computer (CPU)
will seem faster to the user with more
RAM to keep more data and
instructions close at hand for the CPU
Hardware D 19 Yes. Thumb Drives
(a.k.a. USB keys) offer more storage
and quicker data access in a smaller,
more convenient package.

Answers to Chapters 1,2,3,4,5,6,7,8,9 -
End of Chapter ...
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instruction: present so that students
can learn a skill sometimes you may
have to begin at an easier goal that
you know the student can achieve to
build confidence for learning newer
skills. Being organized and having
those skills, as...

Consider the following
instruction: Instruction: AND ...
computer science. In Fig 1, a
structural comparison of organisms
and computer networks is shown. It
can be seen that both show high
similarities. This organization of an
organism is a highly regulated
process from the single cell up to

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